

Figure 1a

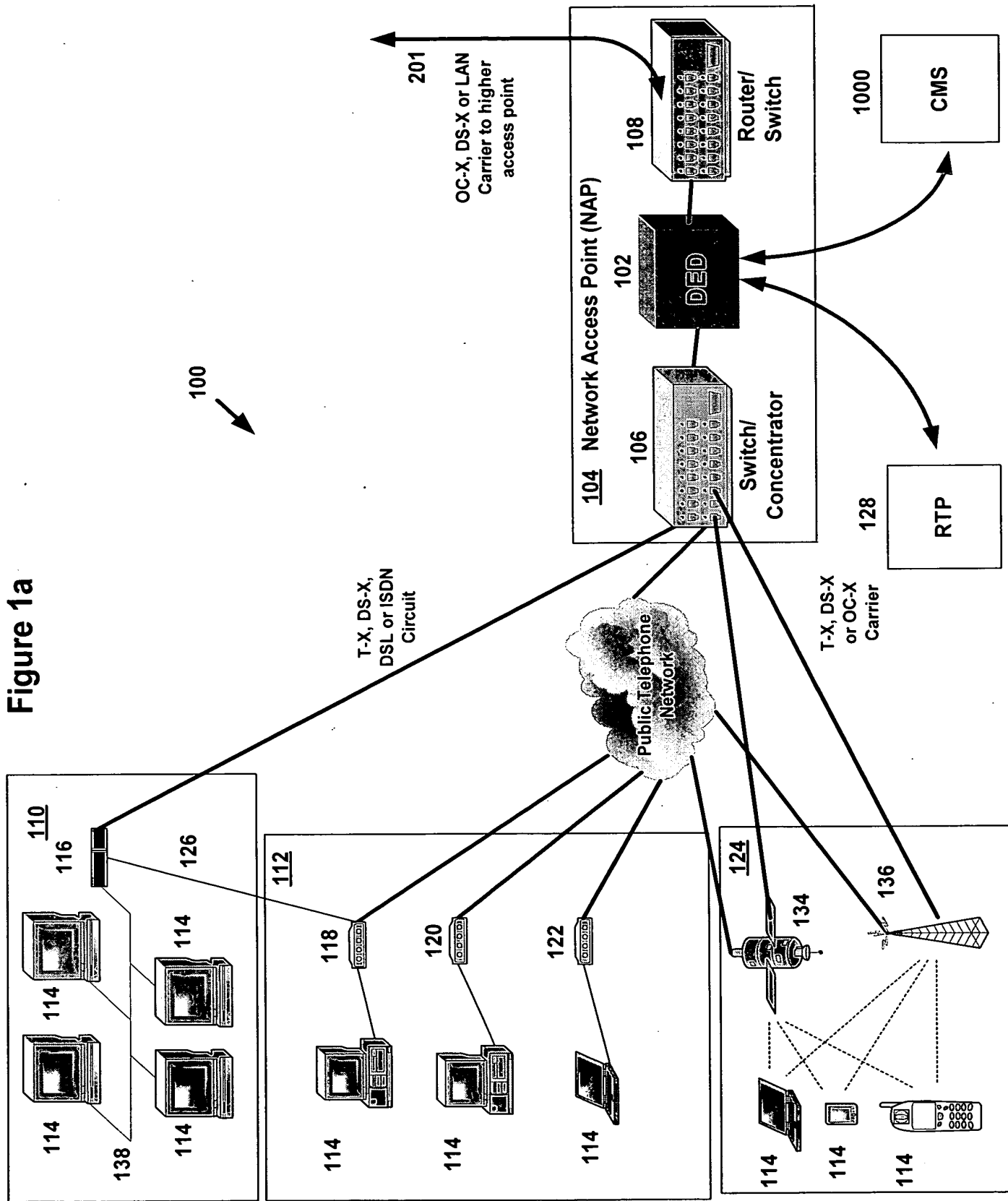


Figure 1b. Regional Transaction Processor

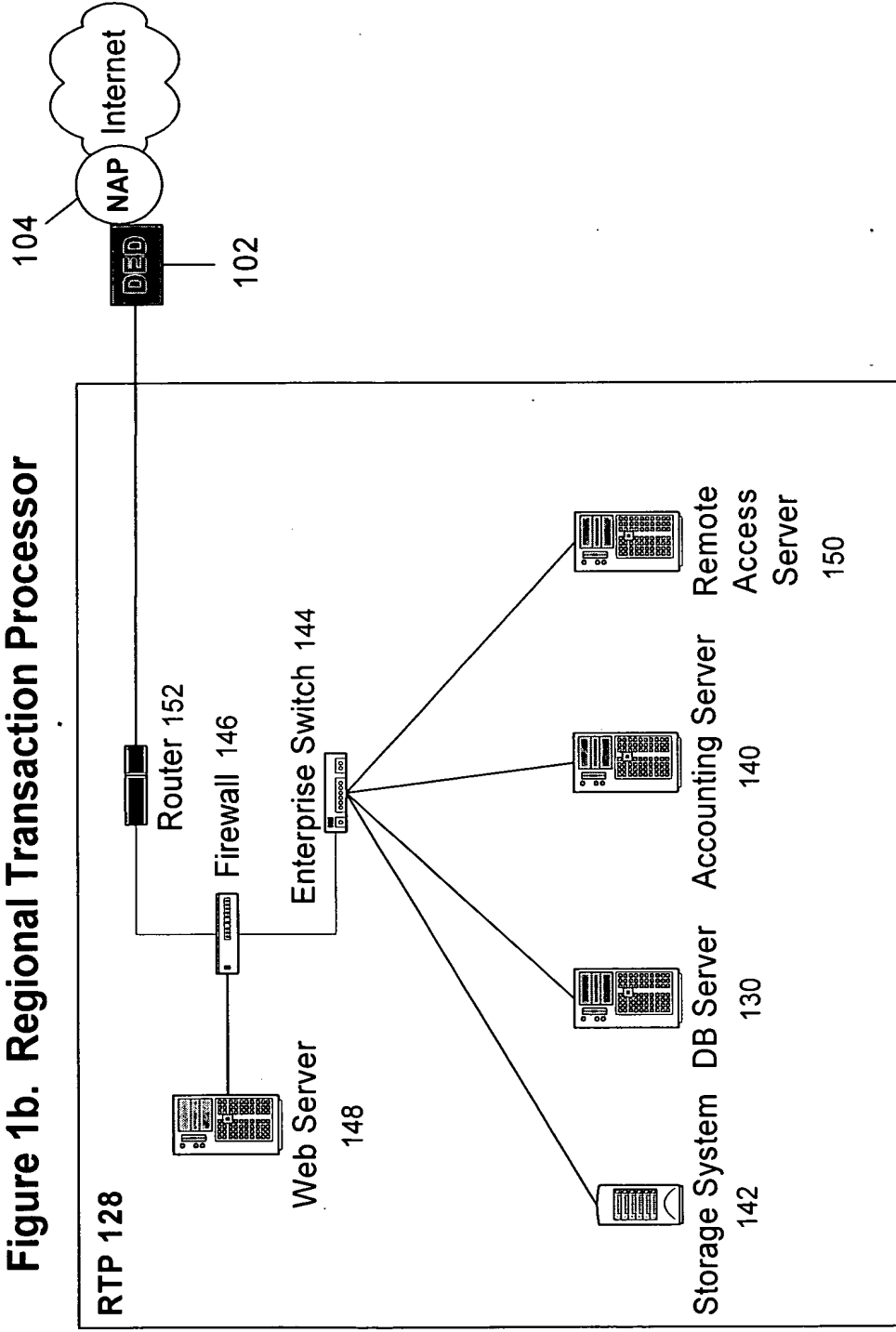


Figure 1c. Content Matching Server

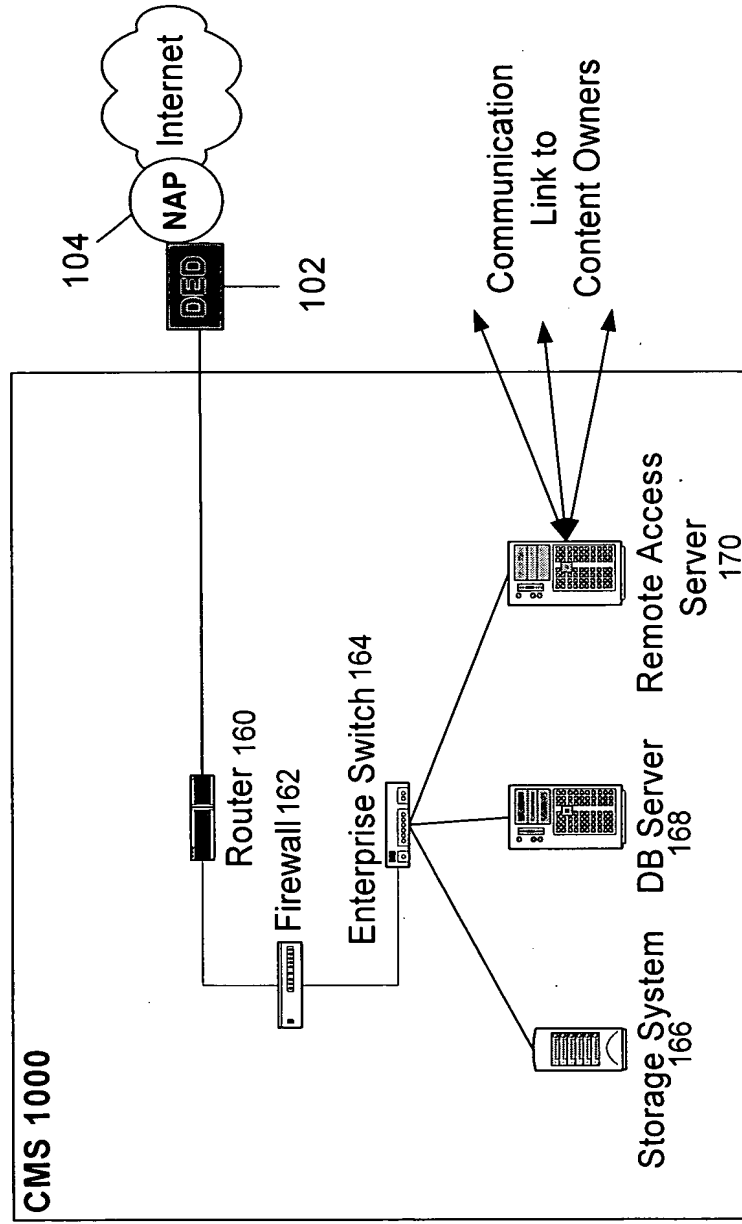


Figure 1d. Central Storage and Backup Systems

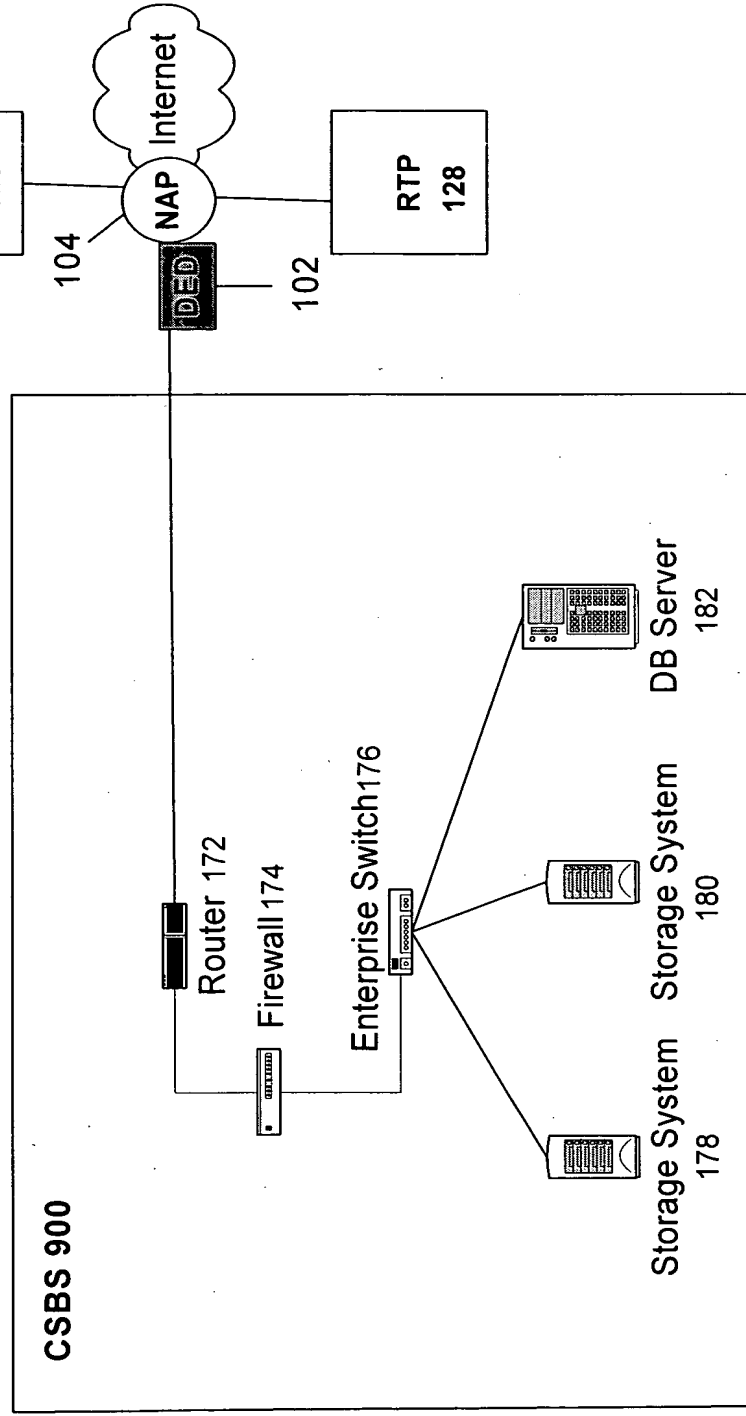


Figure 1e.

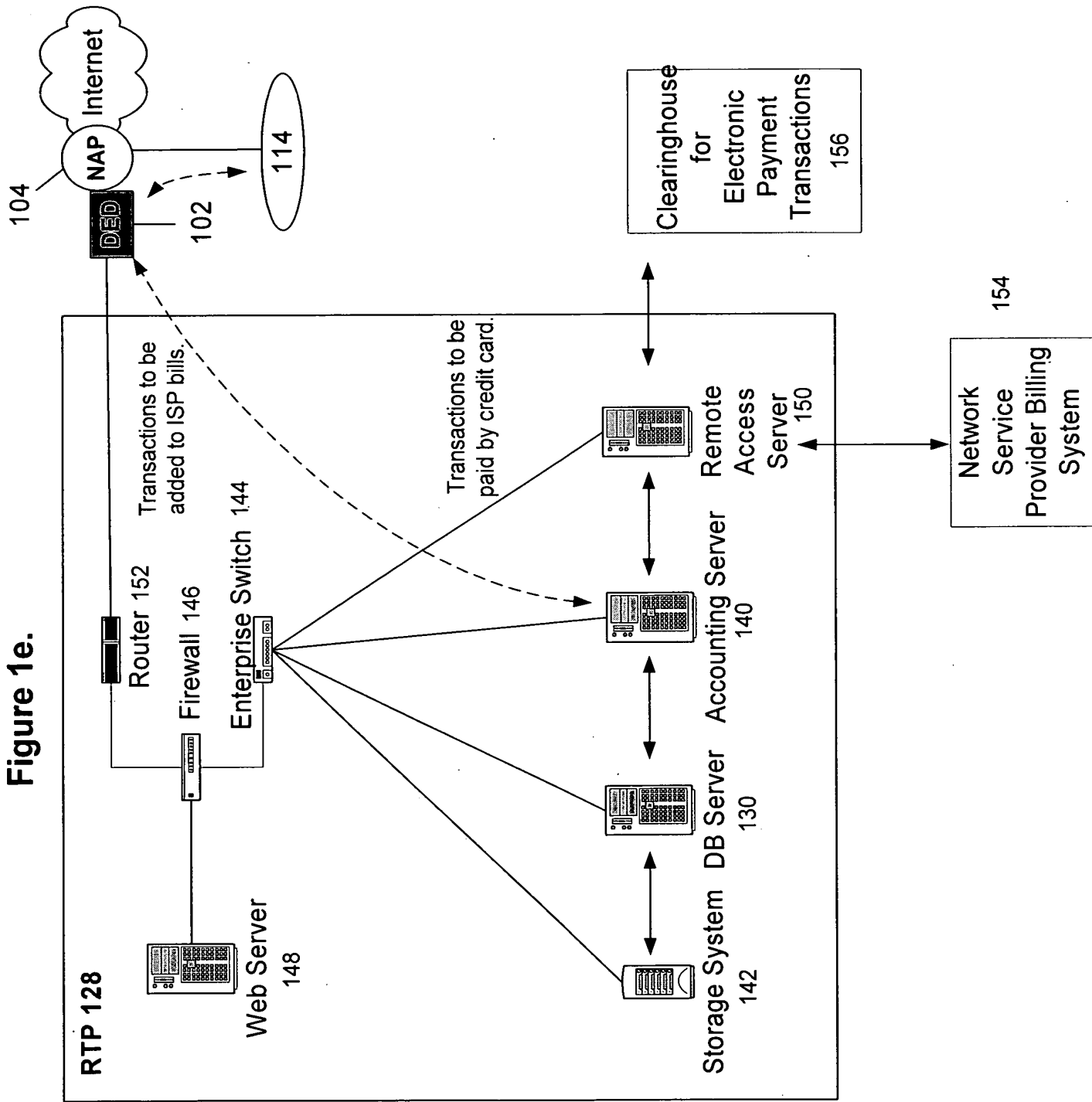
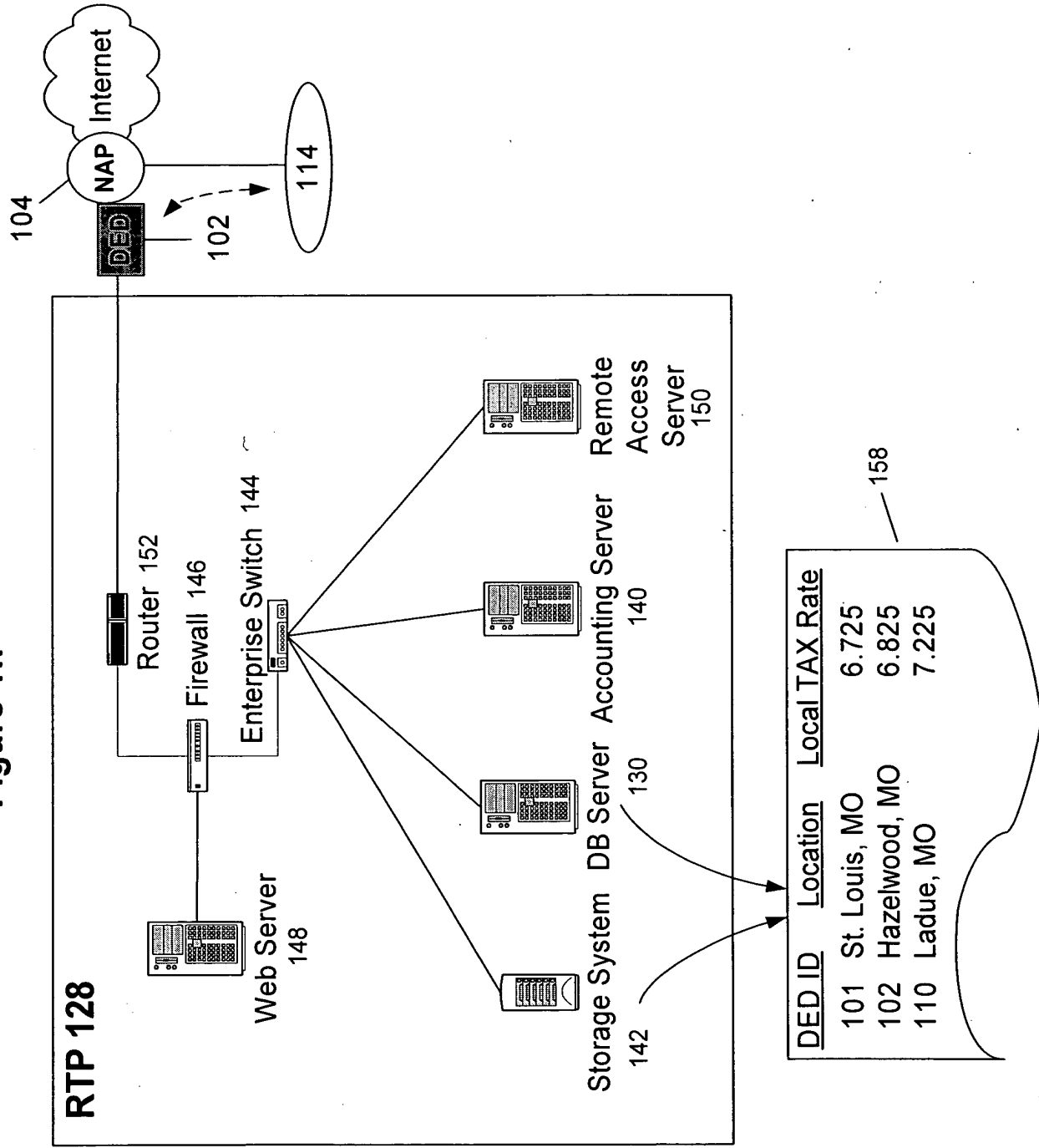


Figure 1f.



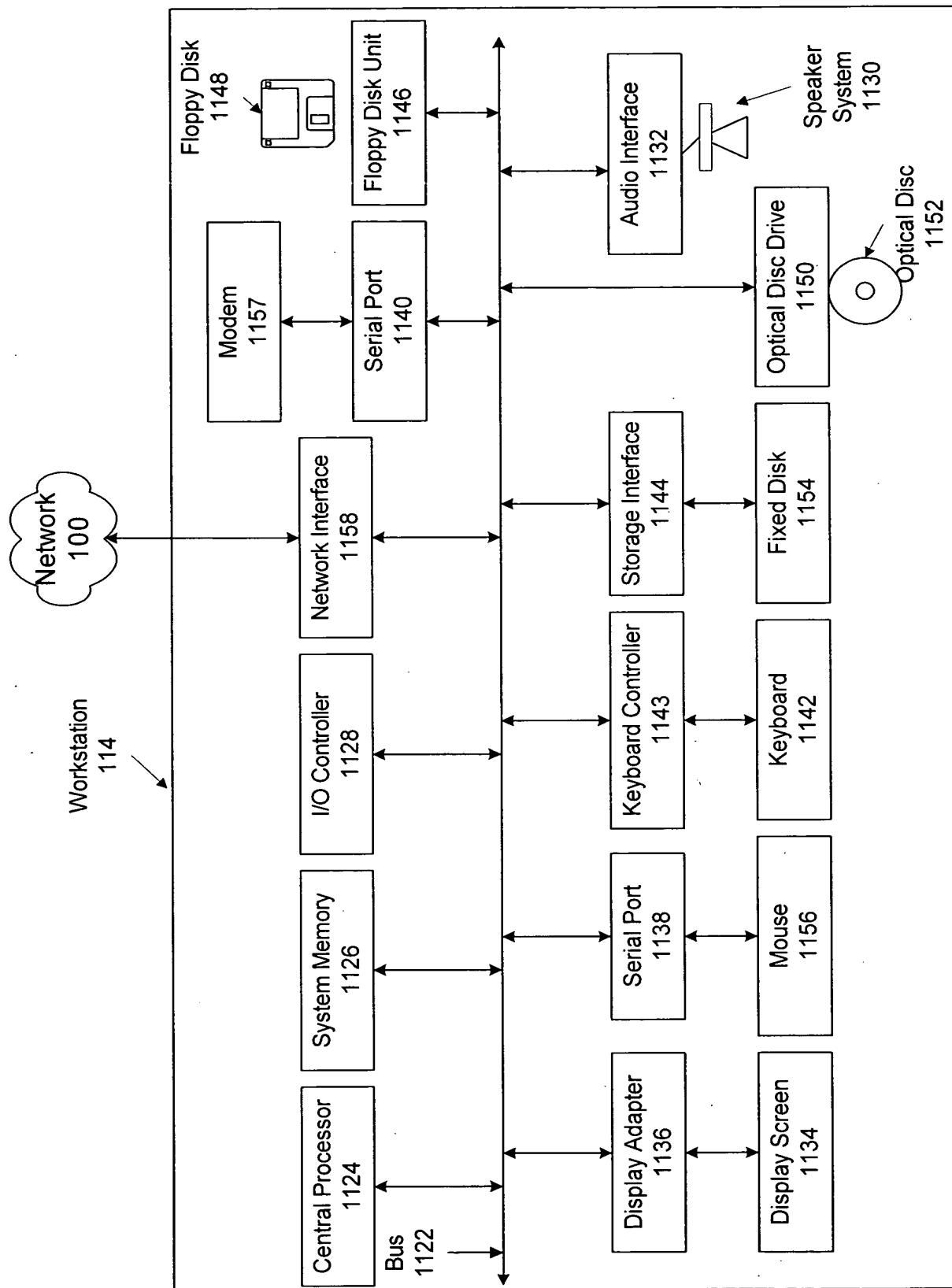


Figure 1g

Figure 2a

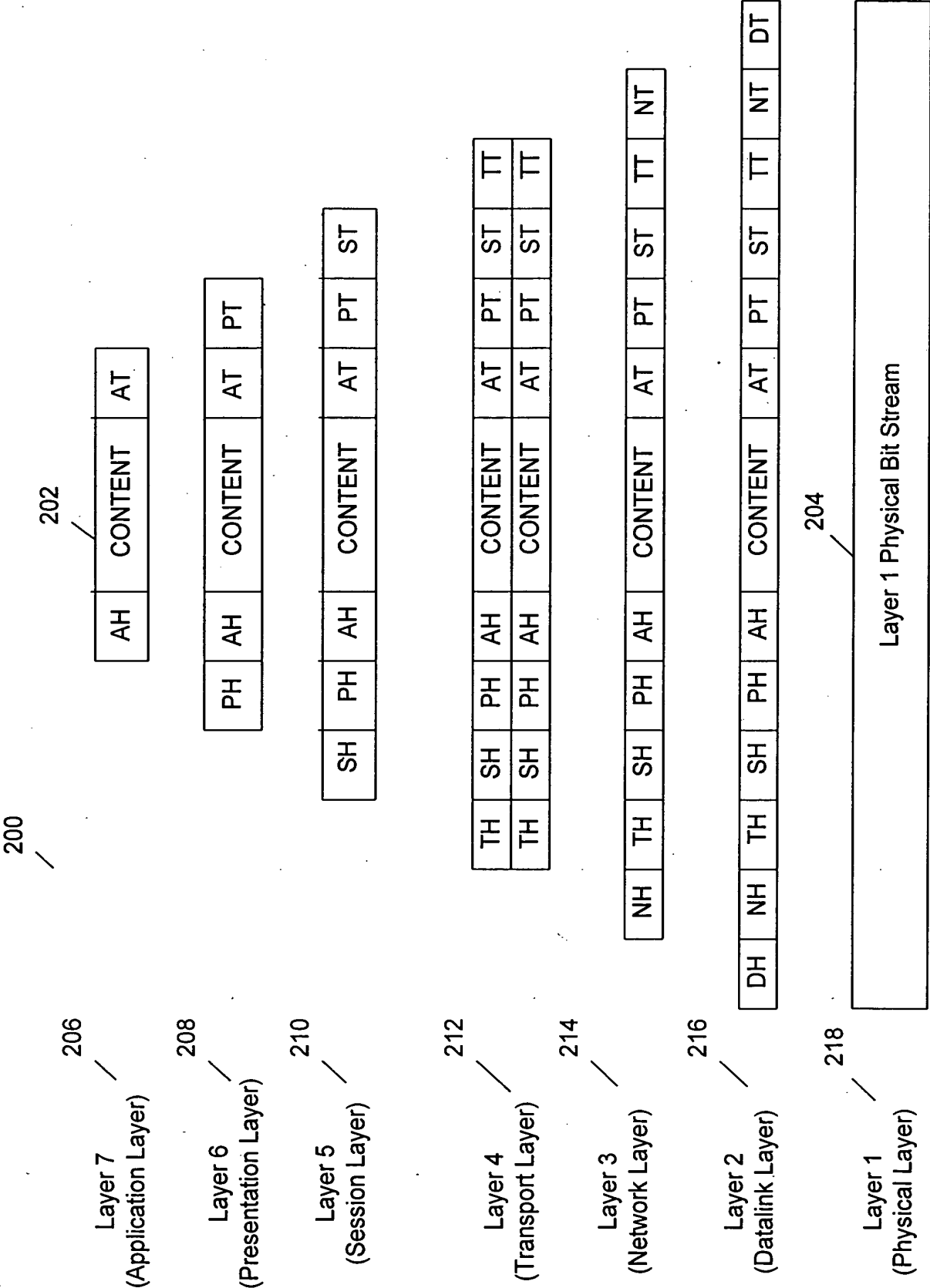
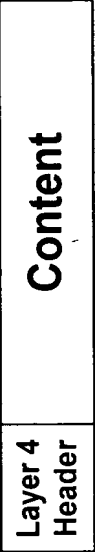


Figure 2b. Layered Data Encapsulation Process

202



TCP, UDP or other
Layer 4 Protocol



IP, IPX or other
Layer 3 Protocol



SONET, ATM,
Ethernet or other
Layer 2 Protocol



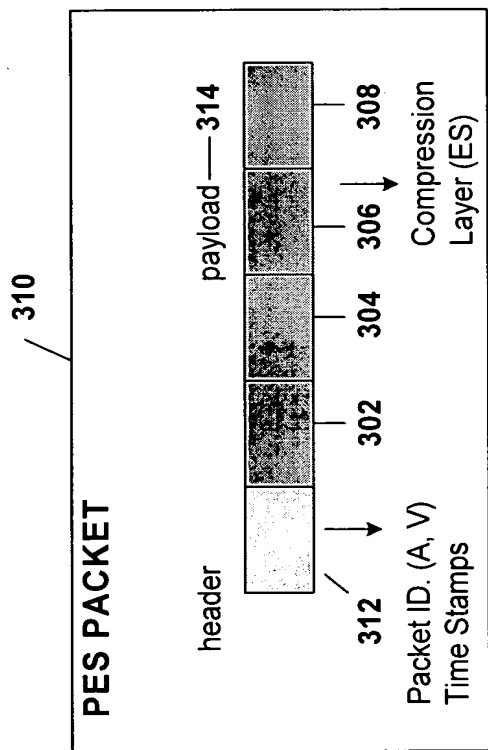


Fig. 3a

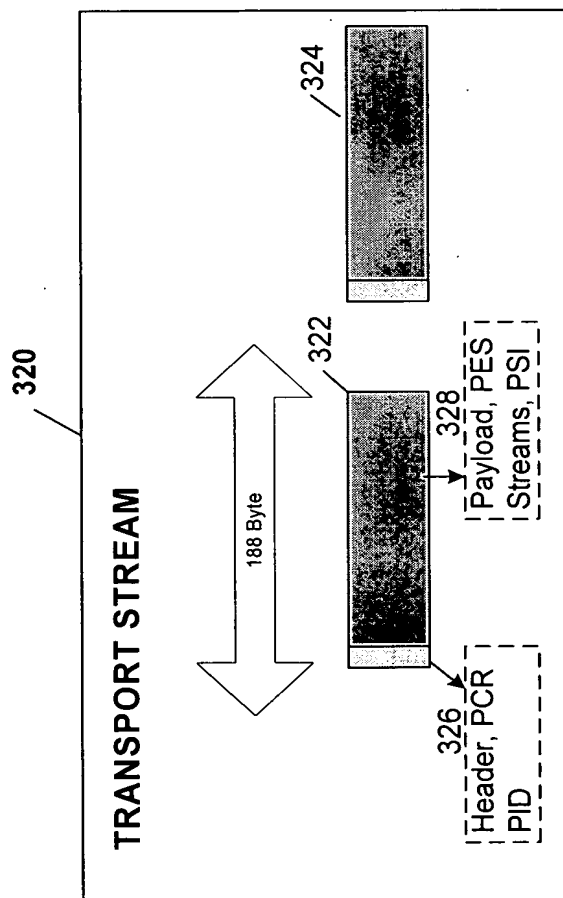


Fig. 3b

Figure 4a

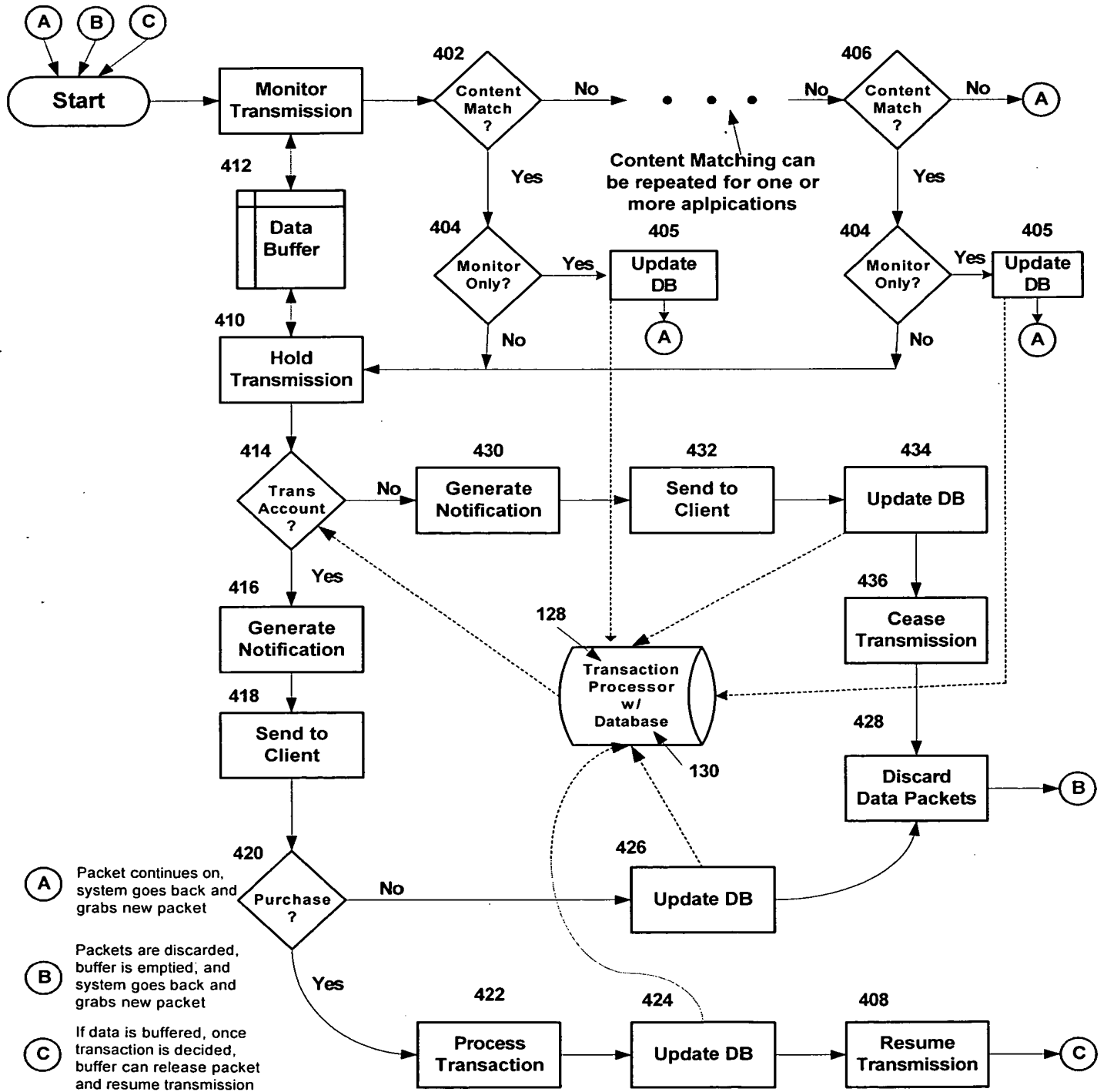


Figure 4b

Purchase Notice

You are attempting to download copyright-protected material.
You have the option to purchase this material for the amount
shown in the Total box below.

Item:	\$2.00
Tax:	.12
Total:	\$2.12

To purchase, please enter your credit card information.

Card Type **VISA** Card # **2222-2222-2222-2222**
Exp. date **07-05**

Or click here to add it to you ISP account ☐

Enter Password **XXXXXXXXXX**

Do you agree to purchase?

Yes **No**

Figure 4c

Purchase Notice

You are attempting to download copyright protected material.
You have the option to purchase this material for the amount
shown in the Total box below.

Item:	\$2.00
Tax:	.12
Total:	\$2.12

To purchase, please enter your credit card information

Card Type

Card #

Exp. date

Do you agree to purchase?

Figure 4d

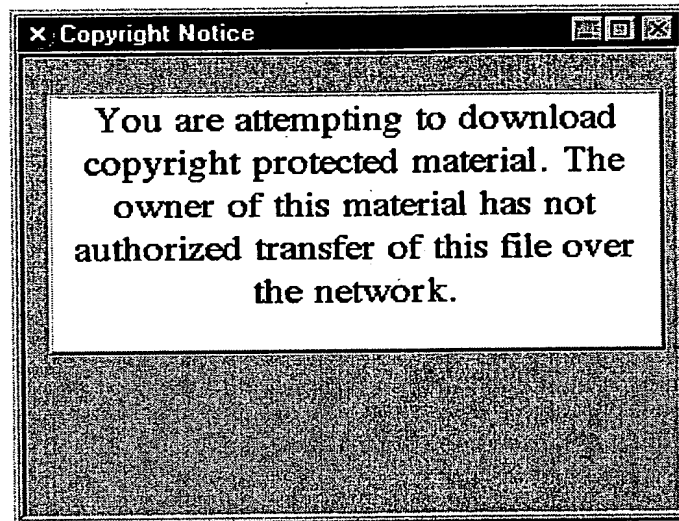


Figure 4e

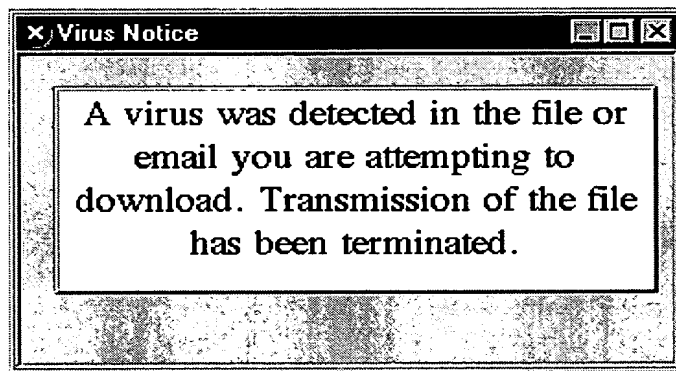


Figure 4f

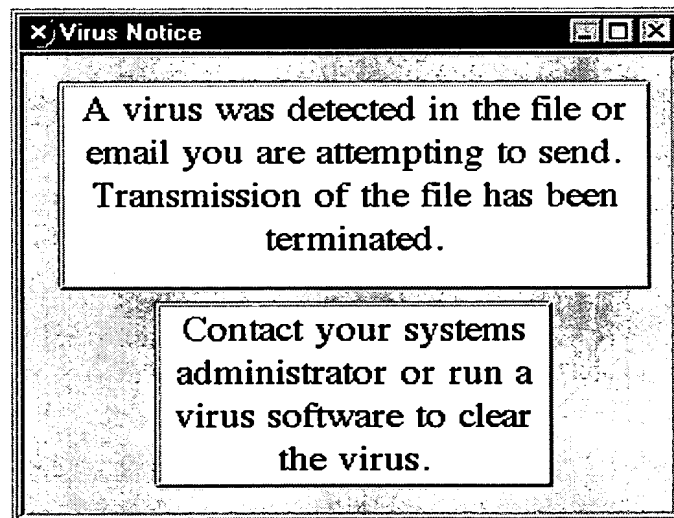


Figure 4g

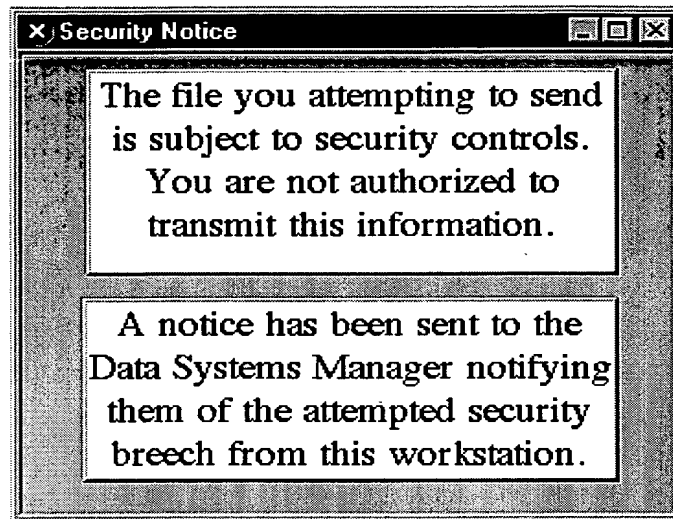


Figure 4h



Figure 4i

Retail Purchase Notice

Your order has been received. Please verify the amount below.

Item:	\$20.00
Tax:	1.20
Shipping:	5.00
Total:	\$26.20

To purchase, please enter your credit card information

Card Type **VISA** Card # **2222-2222-2222-2222**

Exp. date **07-05**

Or click here to add it to you ISP account ☐

Enter Password **XXXXXXXXXX**

Do you agree to purchase?

Yes **No**

Figure 5a

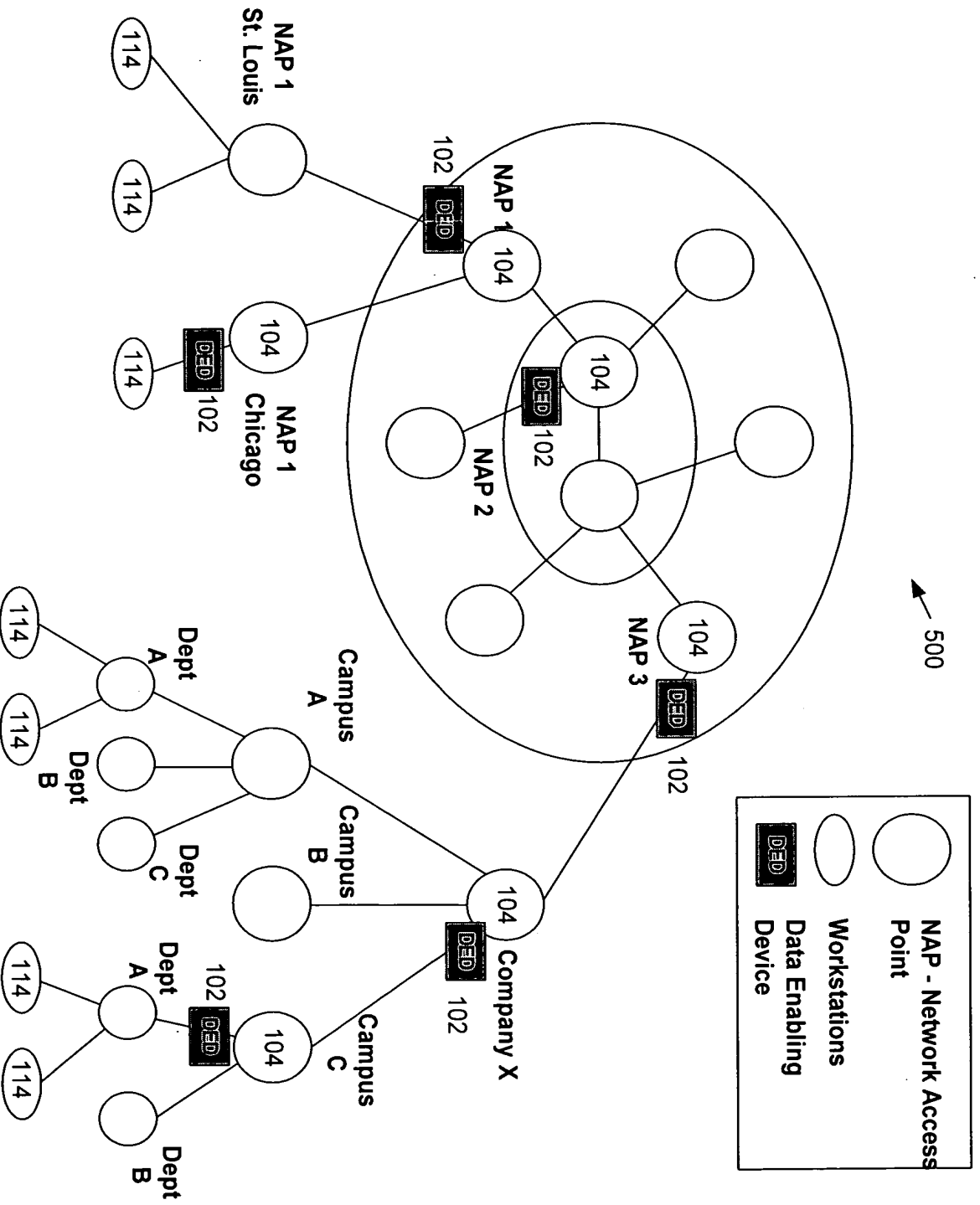


Figure 5b

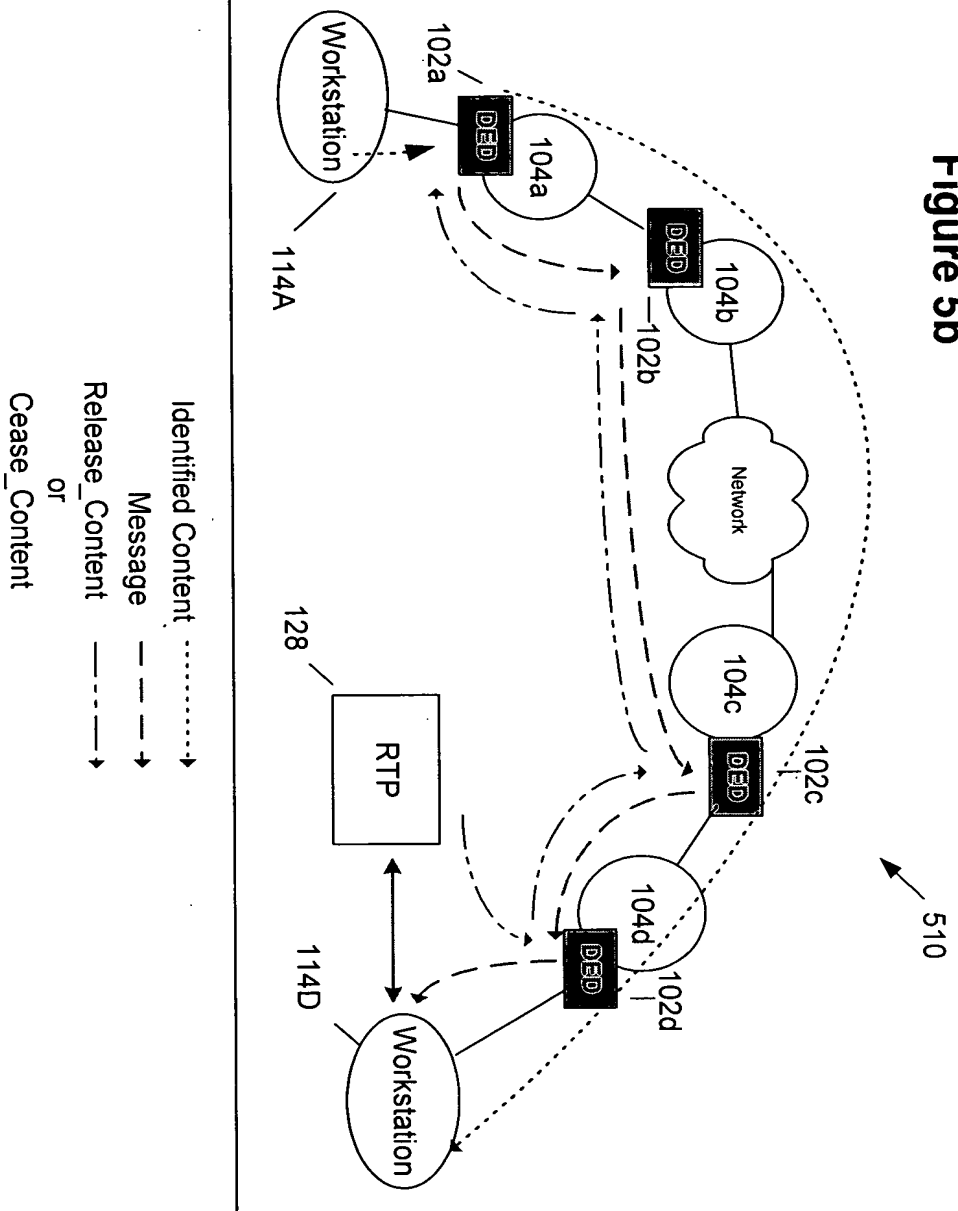


Figure 5c

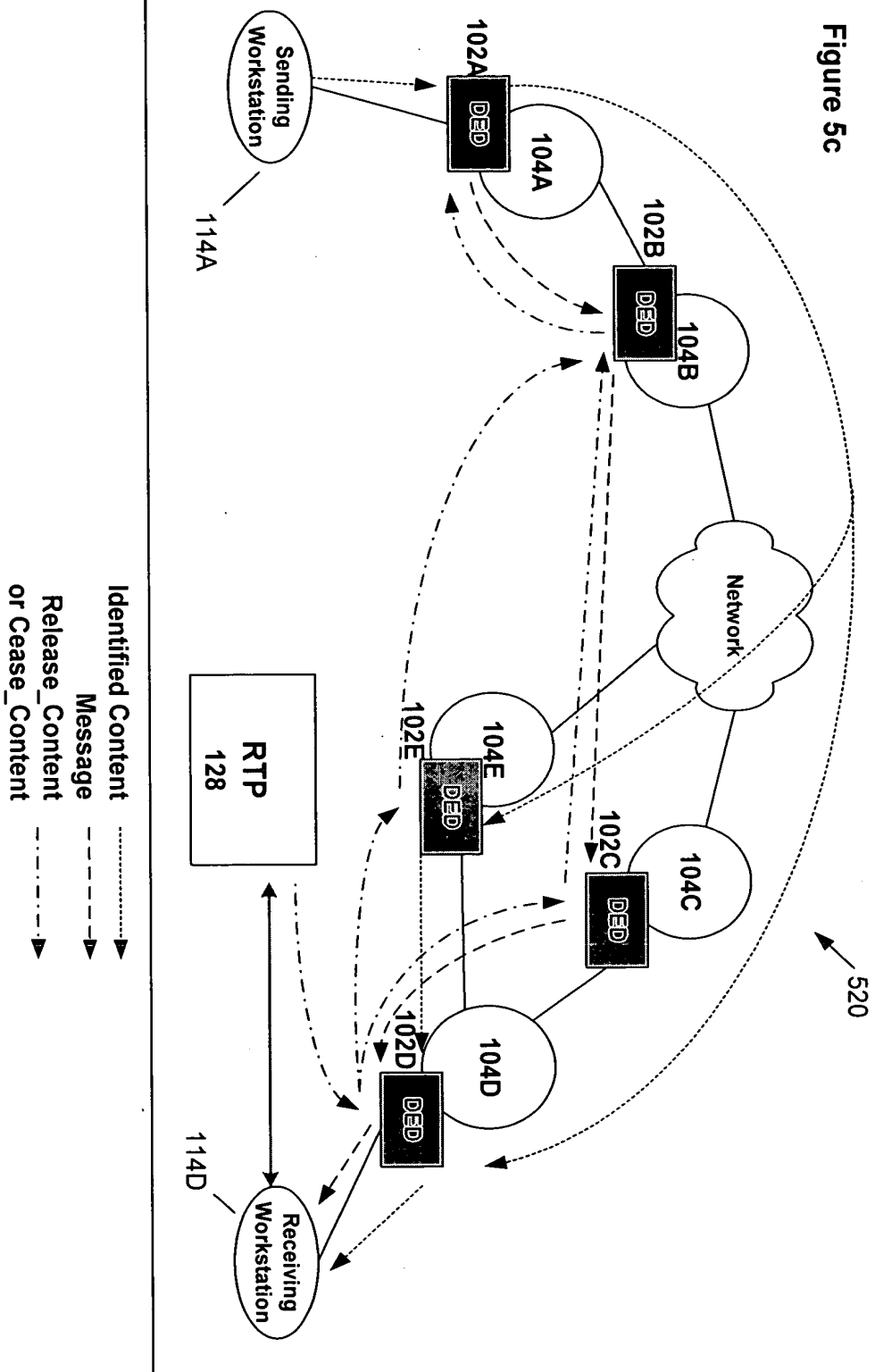


Figure 5d

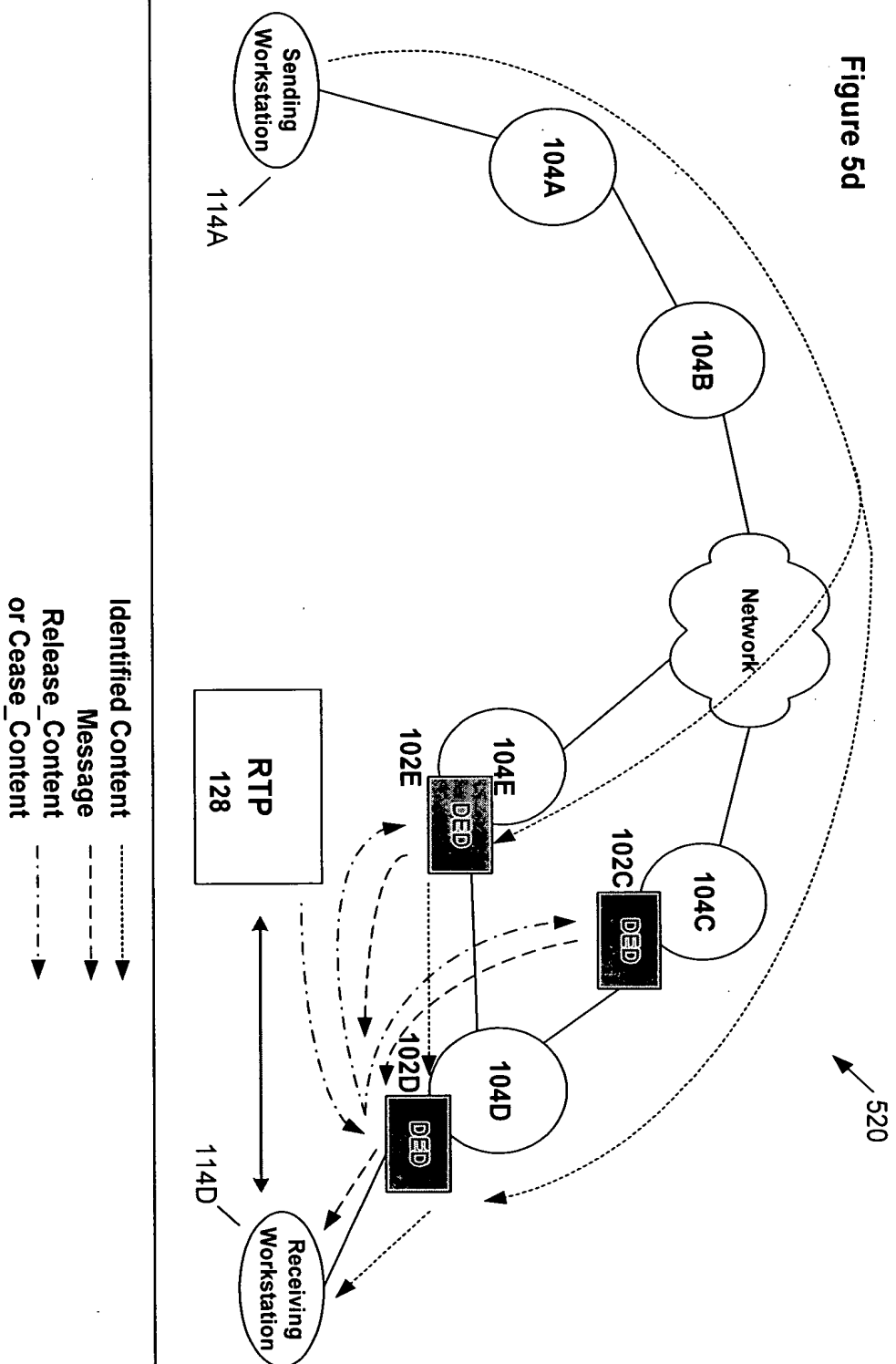


Figure 5e

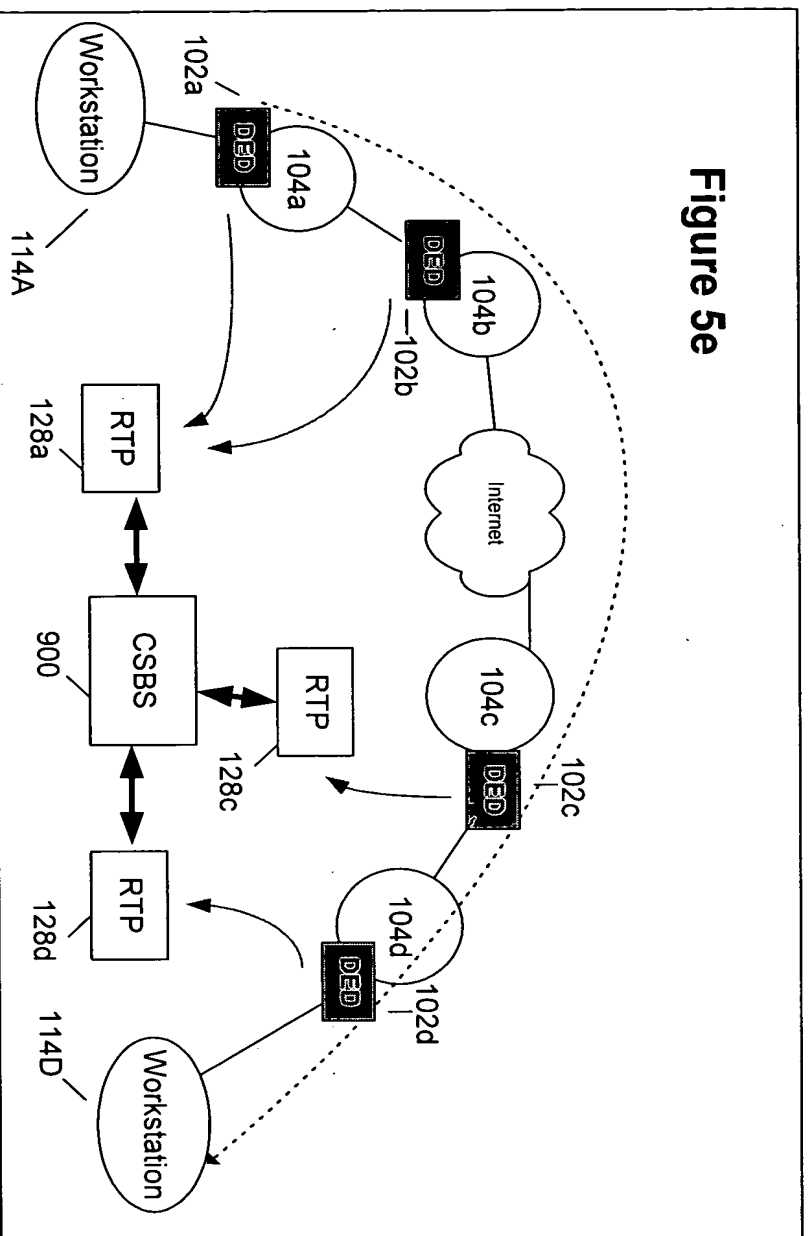
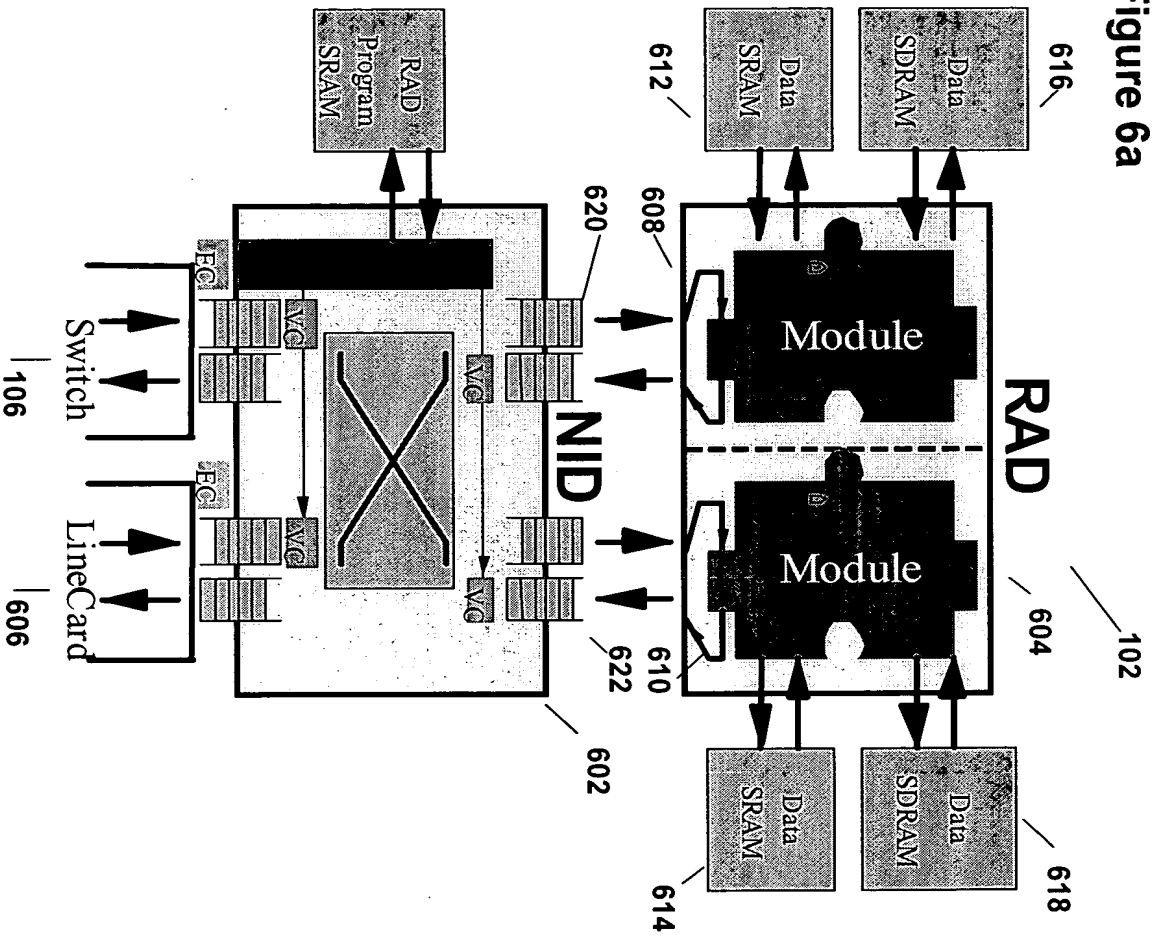


Figure 6a



604

642

SelectMap Programming Interface

640

Control Cell Processor

RAD Program SRAM

644

632

634

630

636

638

Four Port Switch

Virtual Circuit Lookup Table

Synch, Buffered Interface

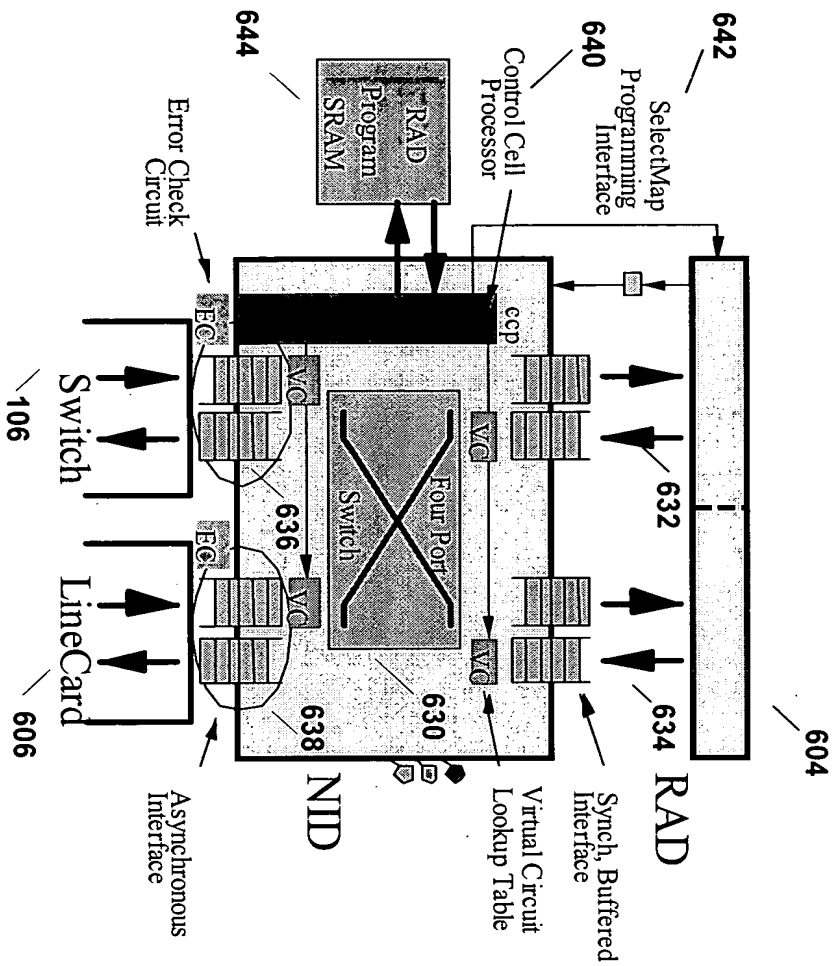
Asynchronous Interface

Switch

106

606

RAD



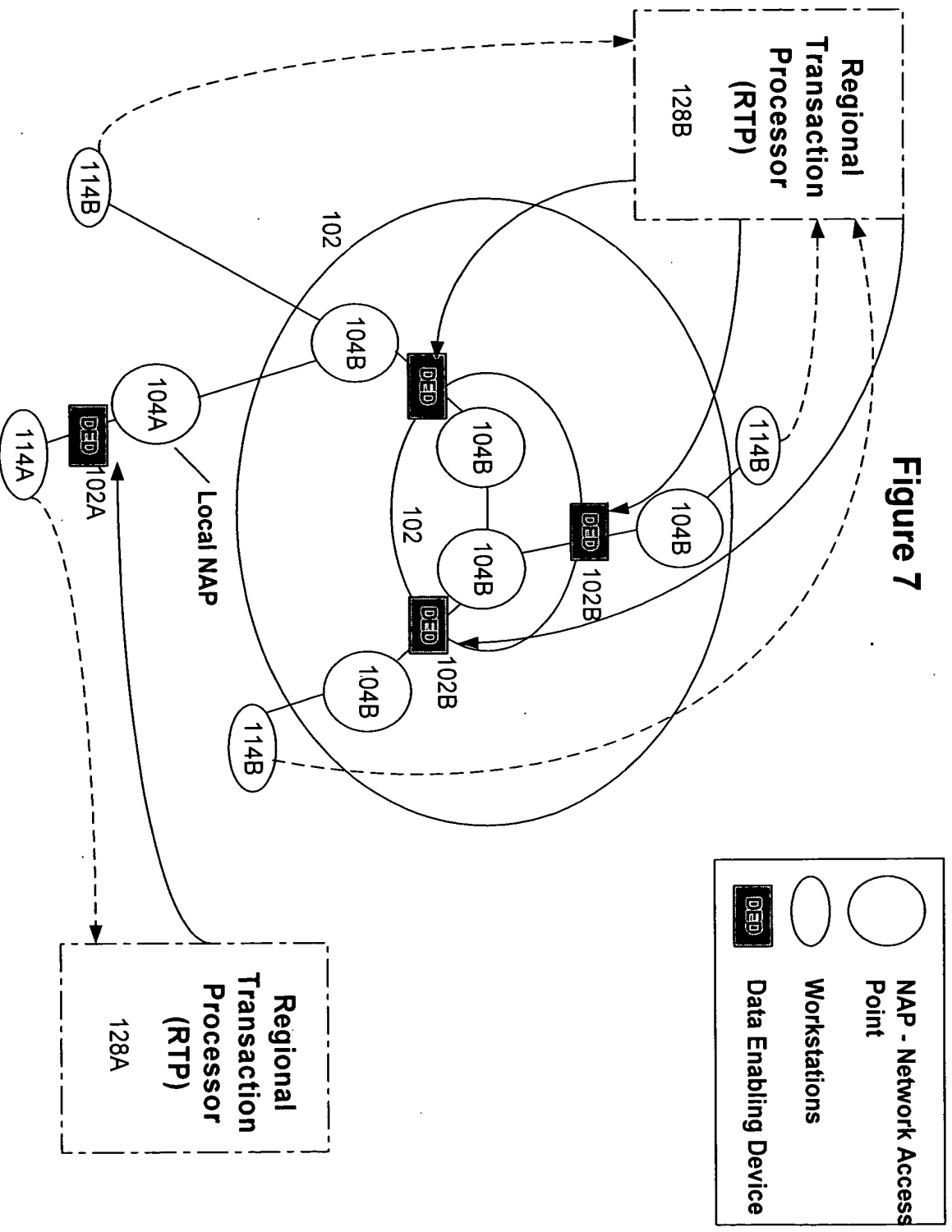


Figure 8a

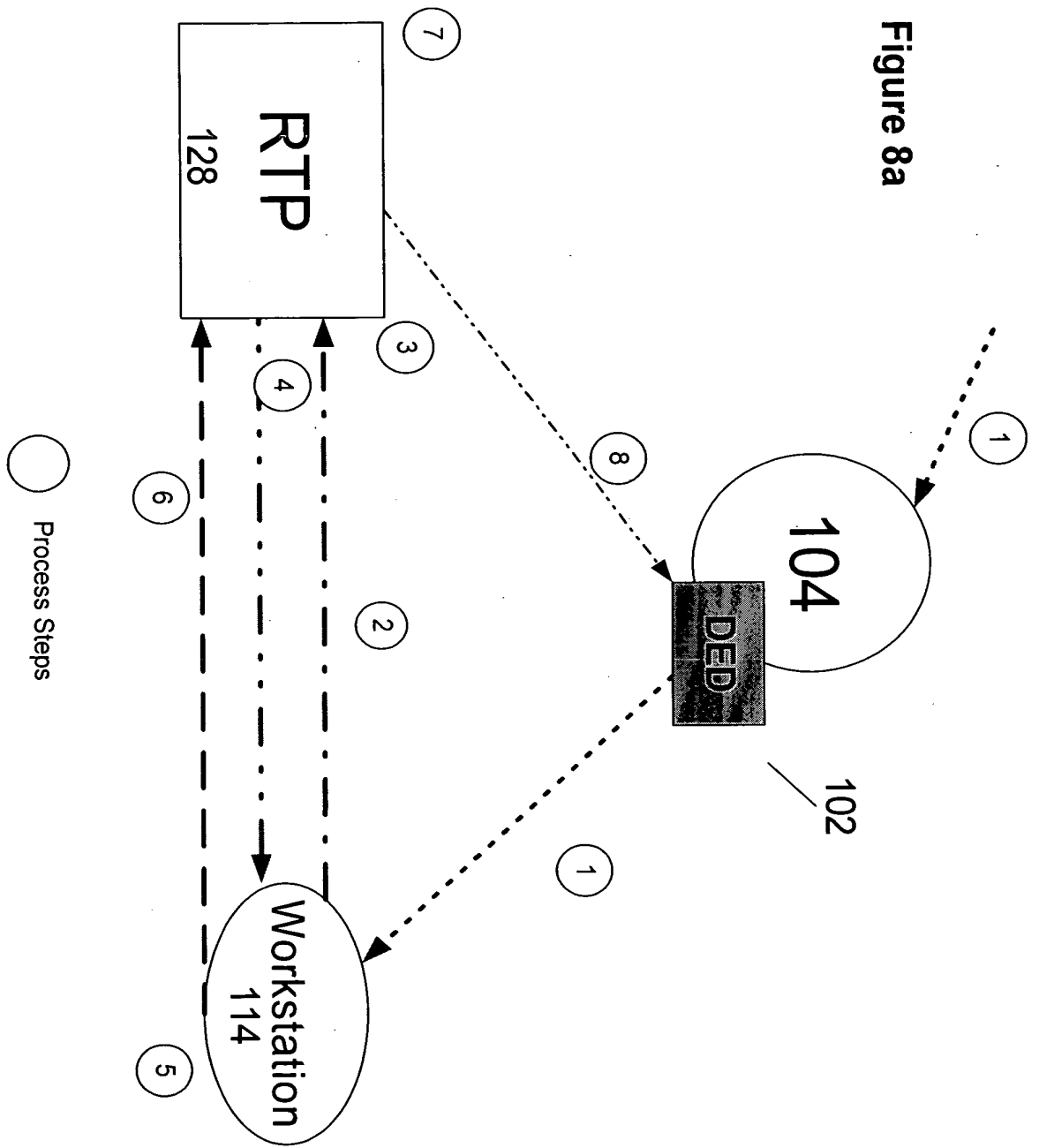
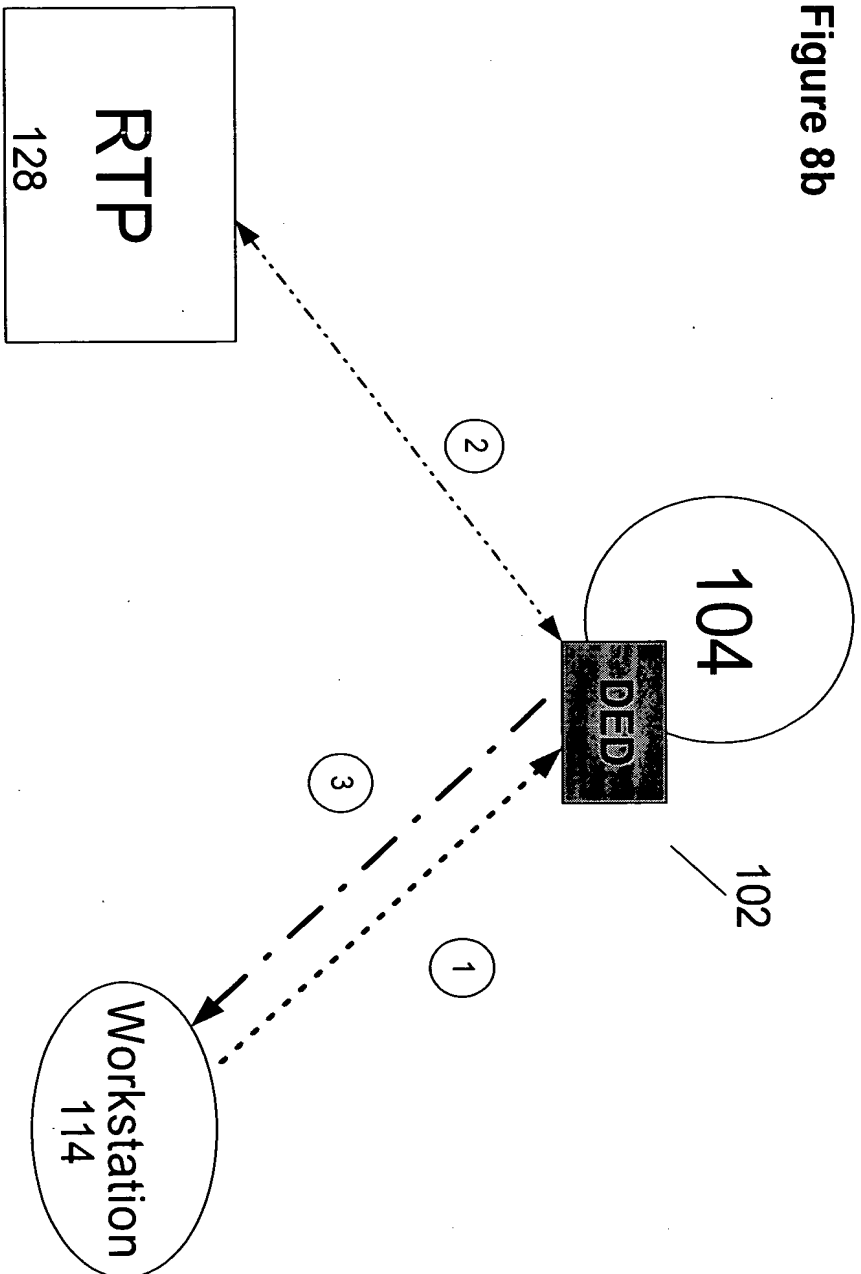


Figure 8b



○ Process Steps 1-3

Figure 8c

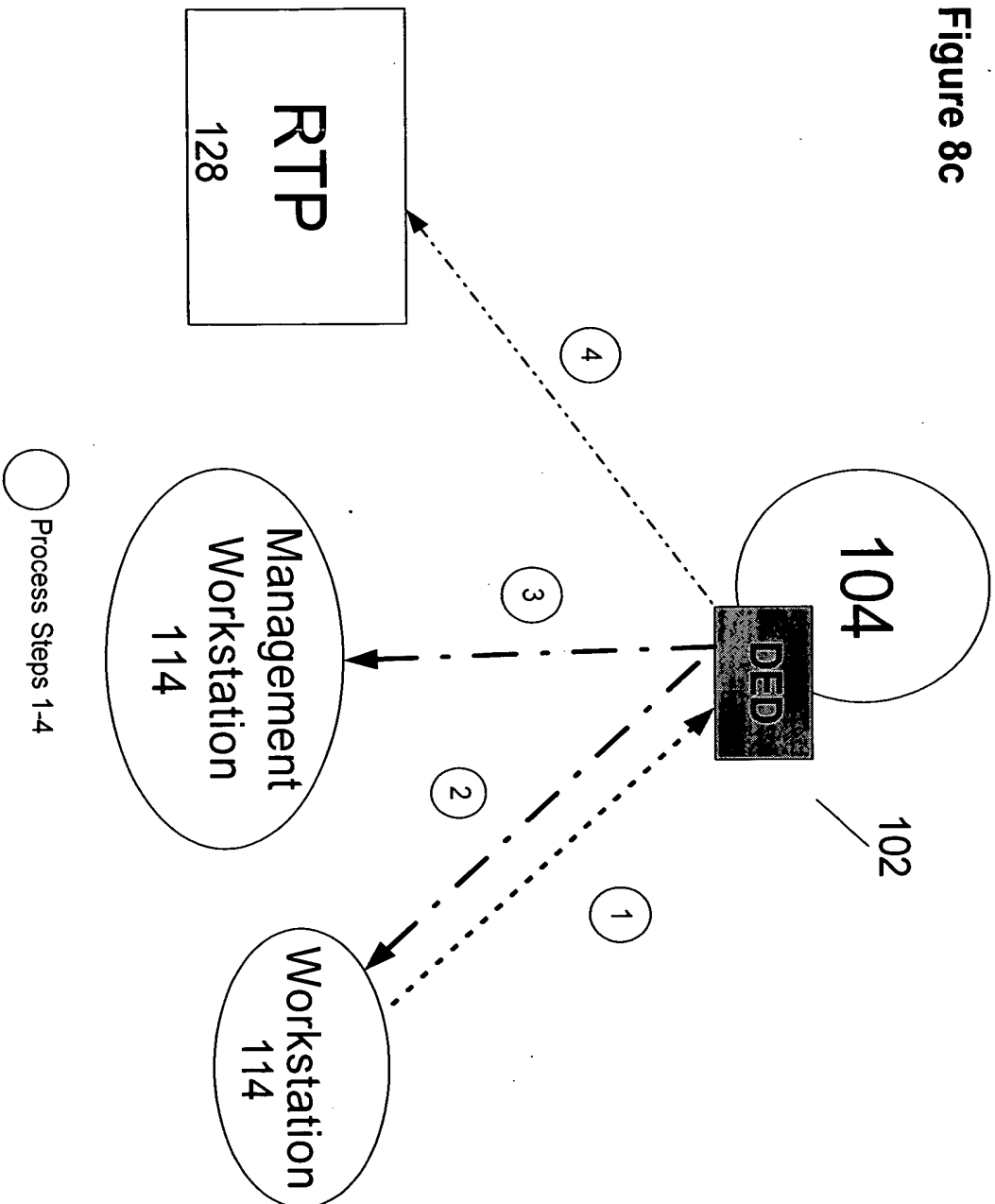
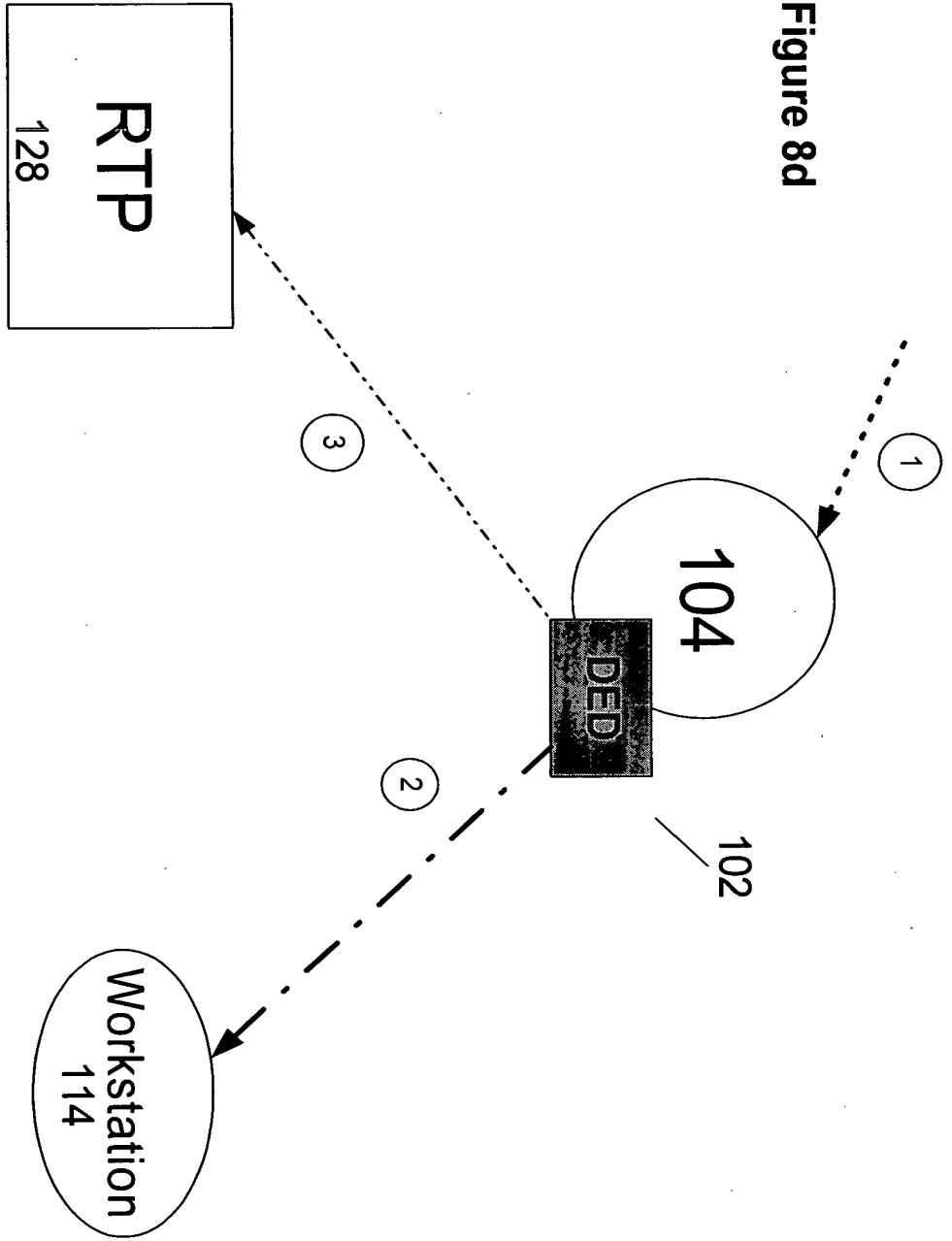
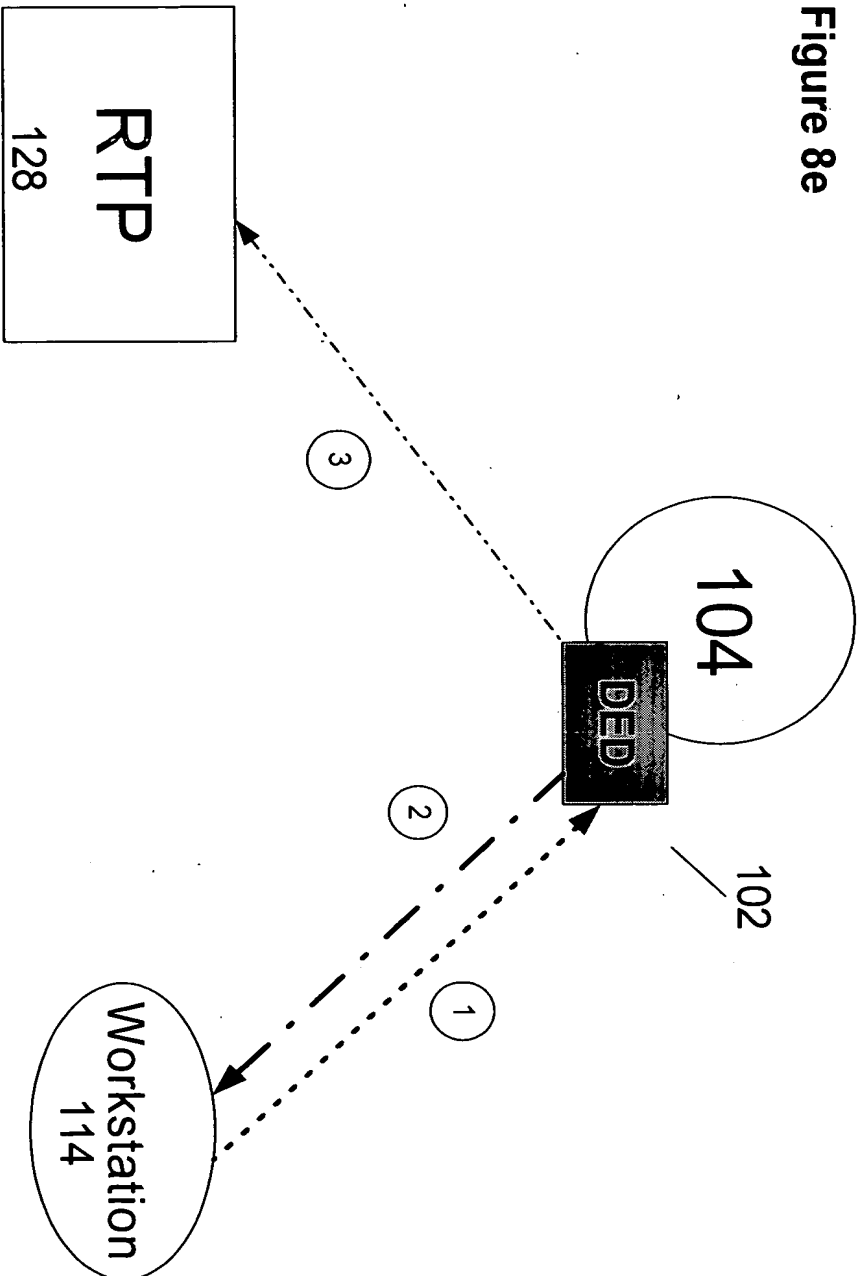


Figure 8d



○ Process Steps 1-3

Figure 8e



○ Process Steps 1-3

Figure 9

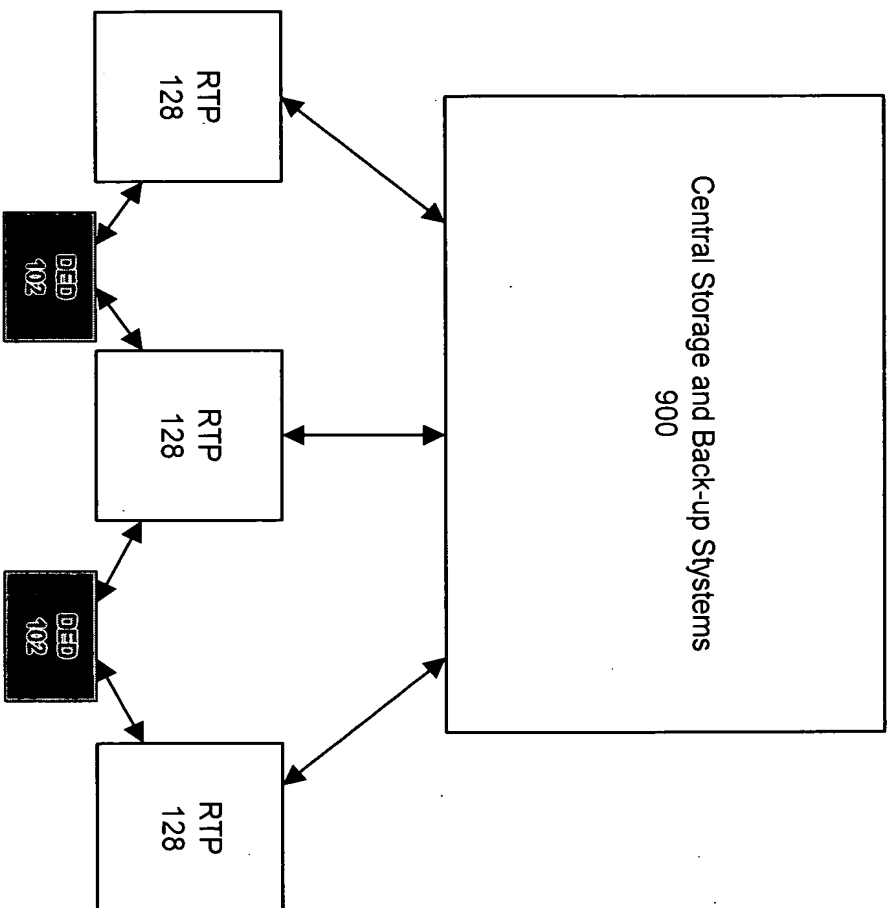


Figure 11

